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L11	6	("5544326" \| "5615344" \| "5684954" \| "5715409" \| "5816718" \| "6185641"). PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/02/21 23:00
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L14	959	web-server	US-PGPUB; USPAT	OR	ON	2005/02/21 23:00
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L18	423	emulator and usb	US-PGPUB; USPAT	OR	ON	2005/02/21 23:00
L19	122	L18 and on-chip	US-PGPUB; USPAT	OR	ON	2005/02/21 23:00
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Jian Shen, Jacob Abraham, Dave Baker, Tony Hurson, Martin Kinkade, Gregorio Gervasio, Chen-chau Chu, Guanghui Hu
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3 ipChinook: an integrated IP-based design framework for distributed embedded systems
Pai Chou, Ross Ortega, Ken Hines, Kurt Patridge, Gaetano Borriello
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Rick Lehrbaum
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Brian K. Schmidt, Monica S. Lam, J. Duane Northcutt
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6 Information migration: A case for monster media
David E. Hailey
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Full text available: pdf(975.14 KB) Additional Information: full citation

7 High-capacity Internet middleware: Internet caching system architectural overview
Gary Tomlinson, Drew Major, Ron Lee
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T. E. Anderson, D. D. Lazowska, H. M. Levy
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10 Yellow Dog Linux on the iMac
Stew Benedict
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Joseph Pranevich
December 1998 **Linux Journal**
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Pravin Bhagwat, Ibrahim Korpeoglu, Chatschik Bisdikian, Mahmoud Naghshineh, Satish K. Tripathi
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Yung-Hsiang Lu, Luca Benini, Giovanni De Micheli
September 2000 **Proceedings of the 13th international symposium on System synthesis**
Full text available: pdf(89.03 KB) Additional Information: full citation, abstract, references, citings

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Mike Esler, Jeffrey Hightower, Tom Anderson, Gaetano Borriello
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Harrison, B.L.;
Computer Graphics and Applications, IEEE , Volume: 20 , Issue: 3 , May-June 2000
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Dahlen, E.; Gustin, J.; Meredith, S.; Moran, O.;
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Pages:1662 - 1676

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7 **A current-folded up-conversion mixer and VCO with center-tapped inductor in a SiGe-HBT technology for 5-GHz wireless LAN applications**
Grau, G.; Langmann, U.; Winkler, W.; Knoll, D.; Osten, J.; Pressel, K.;
Solid-State Circuits, IEEE Journal of , Volume: 35 , Issue: 9 , Sept. 2000
Pages:1345 - 1352

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Samueli, H.;
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Pages:16 - 26

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Goldberg, H.;
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Pages:10 - 13

[Abstract] [PDF Full-Text (400 KB)] IEEE JNL

10 **Solder joint failure analysis using FEM techniques of a silicon based system-in-package**
Goetz, M.; Zahn, B.;
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11 **A 9-M tr. access network system-on-a-chip for mega-bit Internet access at home**
Kozu, S.; Aramaki, T.; Ikeda, C.; Kuroda, Y.; Kawanago, S.; Okada, M.; Kariya, H.; Manabe, M.; Utani, H.; Sudou, E.; Oda, Y.; Suzuki, H.;
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Cole, C.B.; Warwick, T.P.;
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2 **Sun PCi provides the best of two worlds**
Tougaw, D.;
Computing in Science & Engineering [see also IEEE Computational Science and Engineering] , Volume: 2 , Issue: 4 , July-Aug. 2000
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www.ics.uci.edu/~rgupta/publications/cores.pdf

SoftSDV: A Presilicon Software Development Environment .. - Uhlig, Fishtein.. (1999) (Correct) (5 citations)
Accuracy Increasing Speed Processor Modules ISA **Emulator** Resource Analyzer Microarch Simulator IA-64 By caching and reusing the translated code, an **emulator** can dramatically reduce the fetch-and-decode Is A Standard Process On The Host Os Scsi Ethernet **Usb** Controller Source Code Compiler For Ia-64 Cross
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ww1.microchip.com/downloads/en/DeviceDoc/30177s.pdf

pdd: Memory Imaging and Forensic Analysis of Palm OS Devices.. - Goals Palm Os (2002) (Correct)
ROM image can be loaded directly into Palm OS **Emulator** Beware: Parsing against known database formats tags) 2 0 0 2 J. GRAND Related Work Palm OS **Emulator**, www.palmos.com/dev Software-based "virtual (29.1kbps) due to packet generation Currently no **USB** support 38:18 8192 19:09 4096 9:36 2048 4:48 1024
www.first.org/events/progconf/2002/d3-04-grand-slides.pdf

SPIDERS: A Multiprocessor Network Development Testbed - Mongkol Raksapatcharawong.. (Correct)
software and an FPGA-based multiprocessor network **emulator**. We strongly believe that this project is an in the simulator. Section 4 presents an FPGA/**USB**-based network router board and a configuration of board from Altera (www.altera. com) and a **USB** (Universal Serial Bus) development board that
pindex.ku.ac.th/file_research/EECON-Spiders.pdf

Emulation of Multiple EDGE Physical Layers Using a.. - Buckley, Kenyon (2002) (Correct)
line server unit (LSU)a complete mobile station **emulator** (MSE) is formed. In this paper, the can be achieved with the aid of a mobile station **emulator** (MSE)which implements conventional and Telecom Network Lsu Proprietary Connection Ethernet **Usb** Aspe Rf Module Pc Card Figure 1 The Basic
www.ee.ucl.ac.uk/lcs/papers2002/./LCS086.pdf

ECDSA Signature Generation On Constrained Hardware - Fung (2000) (Correct)
Semiconductor. The CY3651 is an in-circuit **emulator** for developing **USB** devices. It contains a 12 MHz The iKey is a small hardwore token that plugs into a **USB** port and is small enough to t on a keychain. Its of the iKey is very similar to the CY3651 **USB** Development System manufactured by Cypress
www.student.math.uwaterloo.ca/~e2fung/report.ps

Rapid prototyping using an FPLD - Ideally After Design (Correct)
and AMD. The larger designs require a hardware **emulator** device containing numerous interconnected FPLDs. CAD tools and a 30,000-gate Zycad/Inca hardware **emulator**. 7 The course also required significant CPU a stereo codec, a PS/2 keyboard and mouse port, a **USB** port, a VGA with a RAMDAC, two banks of 512k 16
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. A mini-tower form factor featuring five **USB** 2.0 **ports**, two IEEE 1394a (FireWire) **ports**, one
.16 **Chip**-to-**Chip** Interconnect .
www.sun.com/desktop/workstation/w1100z/arch-wp.pdf

Versatile Sensor Acquisition System - Utilizing Network Technology (Correct)
Analog to Digital Converters, and enables **Ethernet** communication. We are planning to make freely
Data Are Used. Table 1 :Comparing Midi With **Usb** Midi 3 7 Bits) Midi 2 14 Bits) **Usb** 1.1 (16
sent. 7 www.la-kitchen.fr A 10 Base-T **chip** is connected to the appropriate **Ethernet** compliant
www.suac.net/NIME/NIME04/paper/NIME04_3C01.pdf

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of hardware and software than for example an **Ethernet** or **USB** interface, yet offering enough
and software than for example an **Ethernet** or **USB** interface, yet offering enough performance.
methodology followed in the design of a System on **Chip** solution that is being developed for embedded
www.lboro.ac.uk/departments/el/research/esc-miniconference/papers/sancho.pdf

System-on-a-Programmable-Chip Development - Platforms In The (Correct)
For projects that require networking, a custom **Ethernet** daughter board kit is available. A custom
the functionality of this board including **Ethernet**, **USB**, parallel **port**, serial **port**, analog I/O, and
1 System-on-a-Programmable-**Chip** Development Platforms in the Classroom Tyson S.
www.ece.gatech.edu/~hamblen/papers/SOC_top.pdf

-213 Handout 9: Network Programming - Randal Bryant David (Correct)
www.ece.cmu.edu/~ece845/ics/net-handout.ps

Rapid prototyping using an FPLD - Ideally After Design (Correct)
16 SRAM, a 16-Mbit flash, a video decoder, and an **Ethernet port**. The pricing on this larger board depends
a stereo codec, a PS/2 keyboard and mouse **port**, a **USB port**, a VGA with a RAMDAC, two banks of 512k 16
it difficult for schools to directly use large FPLD **chips**. For ease of use, the FPLDs must be mounted on a
www.ece.gatech.edu/~hamblen/papers/micropaper.pdf

An Inexpensive High Speed Modem for the Universal Serial Bus.. - Thomas Sailer Hb (Correct)
Not Enough Sram Motorola Mpc823 Powerpc Hdlc, **Ethernet** $Bga Package Table 1: April 1999 Market
High Speed Modem for the Universal Serial Bus (**USB**) Thomas Sailer, HB9JNX/AE4WA, and Johannes Kneip,
and since it doesn't contain any memory on **chip**, requires external FLASH and SRAM memories, making
www.ife.ee.ethz.ch/~sailer/ham/dcc99/usb.pdf

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